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U.S. UTILITY Patent Application

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PATENT DATE

APPLICATION NO. 09/780634	CONT/PRIOR	CLASS 713	SUBCLASS 323	ART UNIT 2182 2111	EXAMINER A. J. J.
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APPLICANTS

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TITLE

Power control of a processor using hardware structures controlled by a compiler with an accumulated instruction profile

PTO-2040
12/99

ISSUING CLASSIFICATION

ORIGINAL					CROSS REFERENCE(S)										
CLASS			SUBCLASS		CLASS	SUBCLASS (ONE SUBCLASS PER BLOCK)									
INTERNATIONAL CLASSIFICATION															

<input type="checkbox"/> TERMINAL DISCLAIMER	DRAWINGS		CLAIMS ALLOWED	
	Sheets Drwg.	Figs. Drwg.	Print Fig.	Total Claims
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<input type="checkbox"/> The term of this patent shall not extend beyond the expiration date of U.S Patent. No. _____ _____ _____	_____ (Primary Examiner) (Date)		ISSUE FEE	
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